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To: Commissioner for Patents
Art Unit 2816

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From: Ira Matsil

Pages: 5

Applicant: Terletzki, et al.

Docket No: 00P7882US

Serial No.: 09/659,872

Art Unit: 2816

Filed: September 13, 2000


Examiner: Nguyen, Minh T.

For: Level-Shifting Circuitry Having "High" Output Impedance During Disable Mode

Certificate of Transmission under 37 C.F.R. § 1.8

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Facsimile Cover Sheet with Certificate of Transmission (1 page)
Amendment (4 pages)


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UNDER 37 C.F.R. § 1.116
Art Unit 2816**

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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For: Level-Shifting Circuitry Having "High" Output Impedance During Disable Mode

Response under 37 C.F.R. §1.116

Commissioner for Patents
Washington, D.C. 20231

Dear Sir:

This amendment is being filed in response to the final rejection mailed July 25, 2003.

Applicant further submits that each of the pending claims is allowable without amendment. The remarks presented on the following pages explain the rationale behind this assertion.